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MEI GUO ZE DE MAN Zeidmannbsp;Bob

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Paperback. Pub Date: 2004. Pages: 199 Language: Chinese in Publisher: Beijing Aerospace University Press book introduces the FPGA & CPLD-based digital IC design methodology. The book reviews the history of the development of the programmable logic device CPLD and FPGA detailed structure and its characteristics. Focus on the correct method should be used when teaching design circuits or digital systems. a detailed list of all the preparatory work should be done in the initial stages of design; taken steps in the design process and the principles to be followed in the design how after the completion of the simulation and testing. The book also describes the more popular EDA tools. and also predicted the development trend of programmable logic devices at the same time. The book is suitable to engage in digital IC design engineers and universities electronics class professional ...

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